

## Programme

### Monday 19<sup>th</sup> November 2018

18:00-19:00 Registration

19:00-20:30 Welcome Reception

### Tuesday 20<sup>th</sup> November 2018

08:00-09:00 Registration

09:00-09:15 Opening Ceremony

09:15-10:15 Keynote Presentation

10:15-10:45 Coffee Break

10:45-11:15 **A1P-C:** Poster Session 1 – Short Presentations (*Auditorium – Level 2*)

11:15-12:45 Parallel Sessions

**A1P-C:** *Poster Session 1 (Poster Area – Level 2)*

**A1L-A:** *Analog Circuits and Systems (Auditorium – Level 2)*

**A1L-B:** *Digital Circuits and Systems (Room 3 – Level 1)*

12:45-13:45 Lunch Break

13:45-14:15 **A2P-C:** Poster Session 2 – Short Presentations (*Auditorium – Level 2*)

14:15-15:45 Parallel Sessions

**A2P-C:** *Poster Session 2 (Poster Area – Level 2)*

**A2L-A:** *CAD and Electronic Design Automation (Auditorium – Level 2)*

**A2L-B:** *Nonlinear Systems and Applications (Room 3 – Level 1)*

15:45-16:15 Coffee Break

16:15-18:00 Parallel Sessions

**A3L-A:** *Circuits & Systems for Communications (Auditorium – Level 2)*

**A3L-B:** *VLSI Systems and Applications (Room 3 – Level 1)*

19:00-23:00 Gala Dinner