

Faculty of Information and Communication Technology Department of Communications and Computer Engineering B.Sc ICT (Hons.) in Communications and Computer Engineering Final Year Projects 2009 - 2010

Student Name	Project Title	Supervisor
Borg Ebejer Alison	Intonation Analysis and Modelling of Speech	Prof. Paul Micallef
Busuttil Leonard	Design of an IPTV Video on Demand System	Mr Reuben Farrugia
Cefai Isaac	Microprocessor Operation Simulation	Prof. Paul Micallef
Fessehaye Biniam	Design of a low-voltage Multibit Sigma-delta ADC	Dr Ivan Grech
Formosa Rodianne	FPGA-Based Stepper Motor Controller with RS-232 Interface	Mr Owen Casha
Gambin Isabel	FPGA Silicon Cochlea Modelling	Dr Ivan Grech
Micallef David	Hardware Implementation of Convolutional Encoder with Viterbi Decoder	Dr Edward Gatt
Mifsud Liliana	VHDL Hardware Implementation of an MP3 Encoder Chip	Mr Owen Casha
Portelli Nicholai	Implementation of a Digital Radial Basis Function Neural Net	Dr Edward Gatt
Savona Gilbert	Radiation-Hardening-by-Design of Integrated Circuits	Prof. Joseph Micallef
Schembri Lara	Hardware Implementation of a JPEG Encoder	Dr Edward Gatt
Terribile Olen	The Implementation of a Malicious Wireless Node Localization System on FPGAs	Dr Carl J. Debono
Trapani Sara	Lossless compression of high quality audio and music	Dr Saviour Zammit
Valentino Gianluca	Design and Implementation of an Intelligent Peripheral Controller	Dr Saviour Zammit
Zarb Terence	Implementation of a Baseband GPS Receiver	Dr Ivan Grech