## Sensorless Control of a Matrix Controlled Variable Speed Double Fed Induction Machine





Kris Scicluna Department of Industrial Electrical Power Conversion University of Malta

> Supervised by: Prof. Ing. Cyril Spiteri-Staines Dr Ing. Maurice Apap

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## **Introduction to the Experimental Setup**

Digital Signal Processing Board (DSP) DC Drive Field-Programmable Gate Dourlage Brearth (15 Pt CAn) Machine

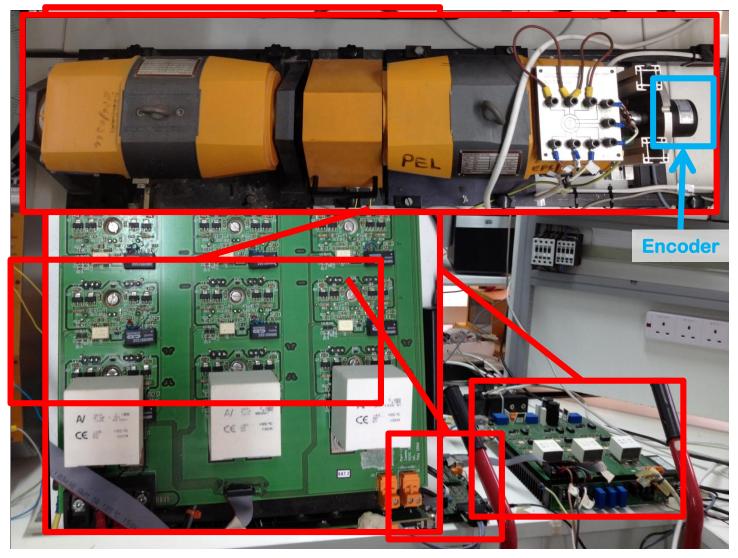


Figure 1 – Matrix Converter with Double Fed Induction Machine Setup

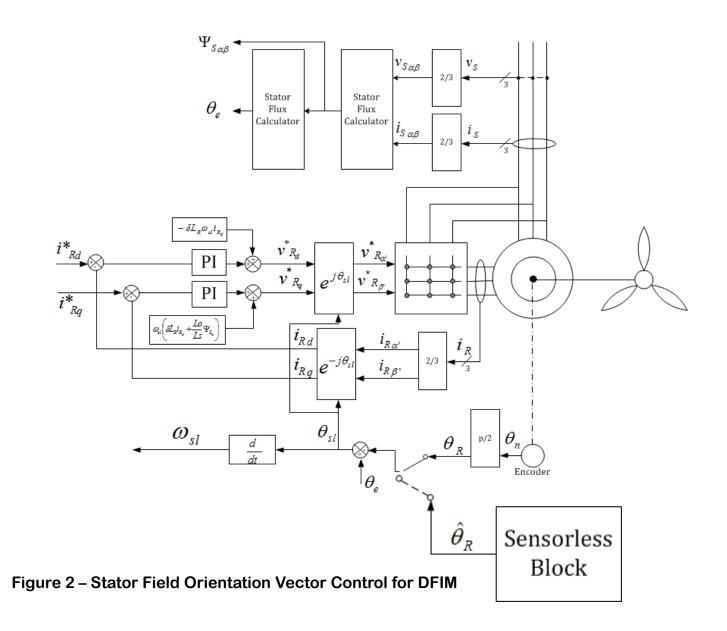
## **Research Questions**

• What type of sensorless techniques can be implemented on the existing experimental setup?

• To what extent do the simulation results of the system reflect the practical ones?

• How does the response of the system using sensorless techniques compare to that when a speed sensor is used?

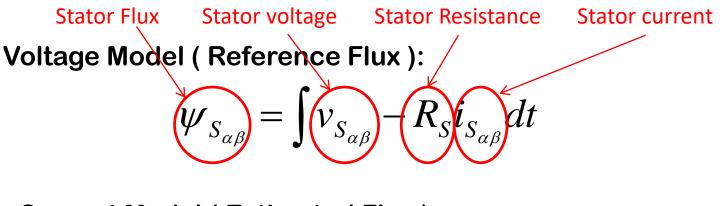
#### Stator Field Orientation (SFO) Vector Control



### <u>Sensorless Block:</u> <u>Model Reference Adaptive System (MRAS)</u>

MRAS is a model based sensorless technique which depends on two mathematical models for the stator flux:

 $\alpha \beta$  subscript denotes stationary frame of reference  $\alpha'\beta'$  subscript denotes rotating frame of reference



**Current Model (Estimated Flux):** 

Estimated rotor frequency

Estimated Stator Flux 🔸

Stator Inductance

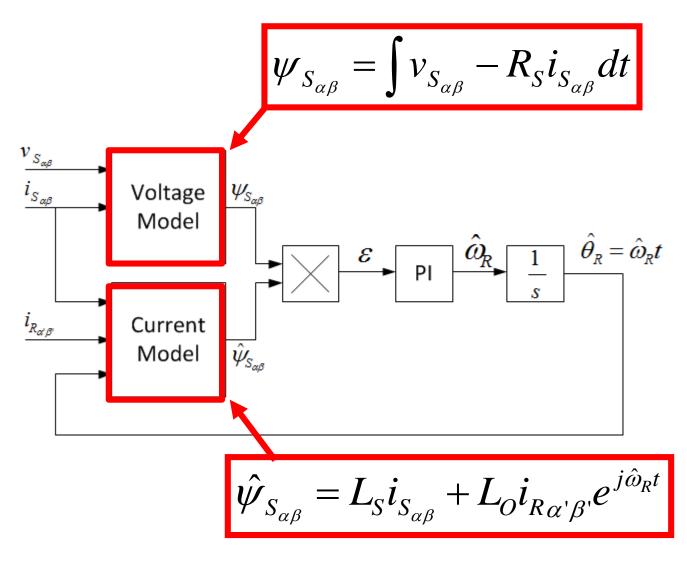
**Mutual Inductance** 

 $\lambda l_{R\alpha'\beta}$ 

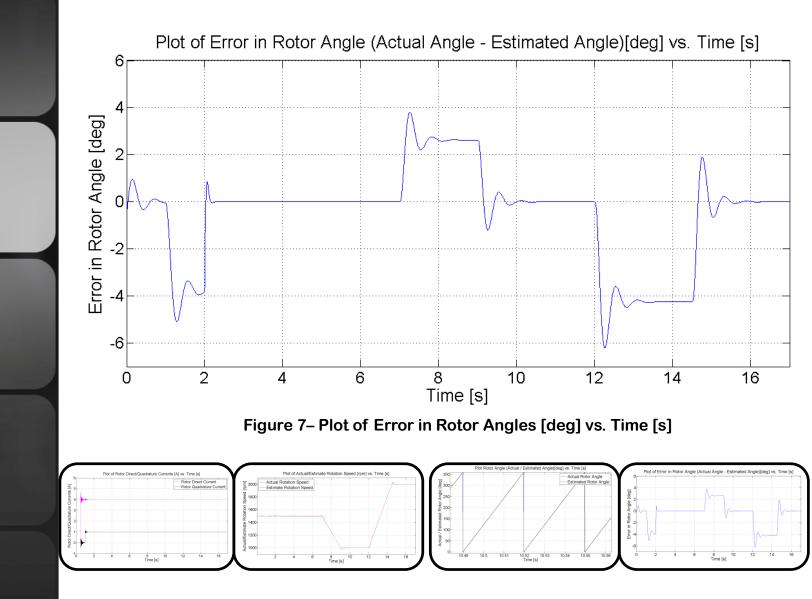
ce Rotor current

Stator current

#### <u>Sensorless Block:</u> <u>Model Reference Adaptive System (MRAS)</u>



#### **Sensorless Block: Simulation Results**





#### **Stator Voltage Model**

The voltage model analyzed previously is:

$$\psi_{S_{\alpha\beta}} = \int v_{S_{\alpha\beta}} - R_S i_{S_{\alpha\beta}} dt$$

In order to use a pure integrator no dc offsets must exist in the stator voltage and current measurements.

Considering stator voltage offsets:

$$\psi_{S_{\alpha\beta}} = \int (v_{S_{\alpha\beta}} + v_{Sof_{\alpha\beta}}) - R_S i_{S_{\alpha\beta}} dt$$

$$\psi_{S_{\alpha\beta}} = \psi_{Si_{\alpha\beta}} + \psi_{Sof_{\alpha\beta}}$$

Where:

 $\Psi_{Si_{lphaeta}}$  Ideal stator flux without offsets

 $\psi_{Sof_{\alpha\beta}}$  Stator flux offset component due to voltage offset

#### **Stator Voltage Model Band-pass filter approximation**

•Second order band-pass filter can be used to approximate Integrator.

•Signals at the input of band-pass are at 314.159 rad/s (50 Hz)

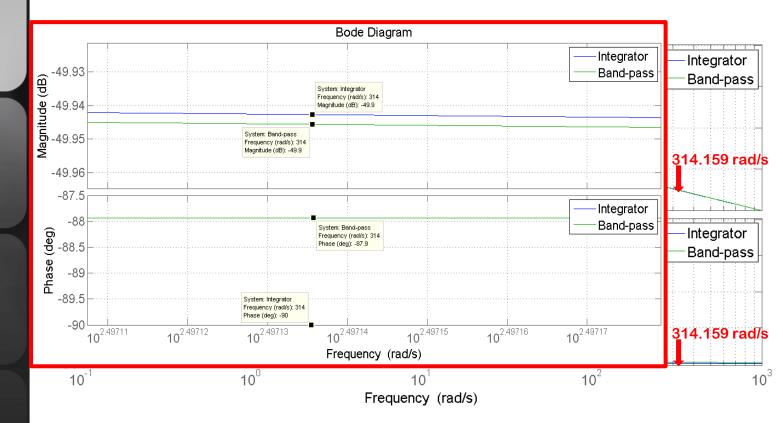
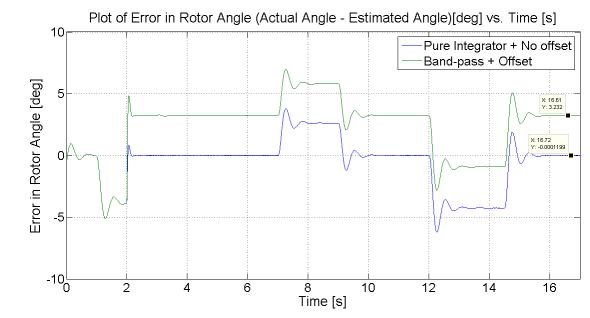


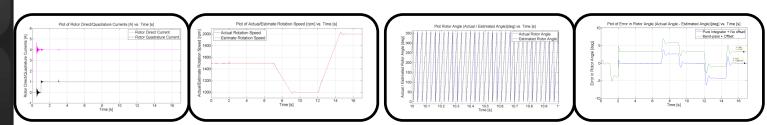
Figure 8– Bode of Plot for Integrator / Band-pass

#### Stator Voltage Model: Simulation Results

Simulations in Simulink© were repeated with the voltage<br/>model using the band-pass filter for the following<br/>conditions:Ird\* = 4 A at 0.5 s<br/>Irq\* = 1 A at 1 sShaft speed = 1500 rpm at 0 s<br/>MRAS started at 2 s

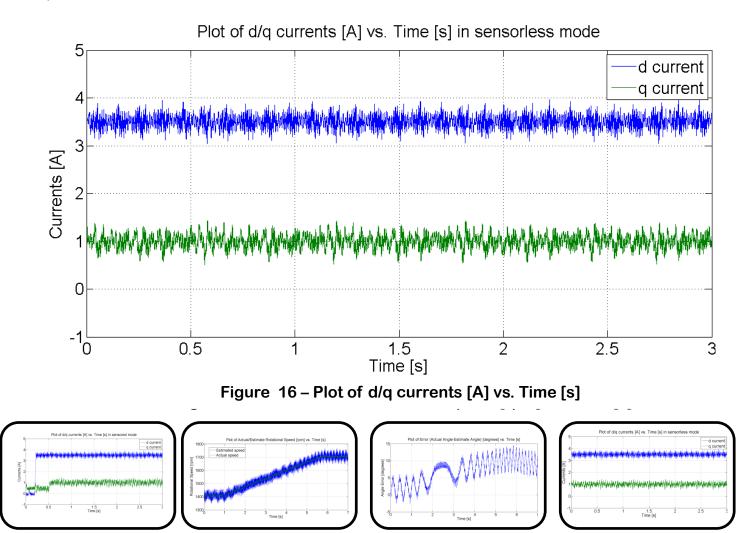






#### **Practical MRAS Results**

Practical Results obtained by applying MRAS on experimental set-up with the following conditions: Ird\* = 3.5 A Initial Shaft speed = 1500 rpm Irq\* = 1 A



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#### **Further Work**

• Introduction of inductances in series with rotor windings to reduce harmonics.

•Analysis of low frequency harmonic in practical estimated angle error.

•Improved stator voltage models to eliminate dc offsets measurements without steady state error.

# **End of Presentation**

